## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(remaining battery power) and (level shifter circuit) and (N channel MOS transistor)	US-PGPUB	WITH	OFF	2006/03/30 15:59
L2	0	(remaining battery power) and (level shifter circuit) and (N channel MOS transistor)".clm"	US-PGPUB	WITH	OFF	2006/03/30 15:59

3/30/2006 3:59:42 PM C:\Documents and Settings\PTibbits\My Documents\EAST\Workspaces\10815444.wsp